

WSA ✓

3rd Workshop on System Analysis & Verification
December 2, 2016 | Islamabad, Pakistan

Timings

6:00pm to 8:00pm

Location

Seminar Hall
PG Block
SEECS-NUST, Sector H-12
Islamabad, Pakistan

General Program Chair

Dr. Osman Hasan

Program Committee

Gen. Co-Chair/Web Coordinator

Faiq Khalid Lodhi

Technical & Publicity Chair

Adnan Rashid

Registration Coordinator

Asad Ahmed

Local Arrangement Chair

Rashid Khalid Lodhi

Organized by

**System Analysis and
Verification (SAVe) Lab**

Application Specific Integrated Circuits (ASICs)

Application Specific Integrated Circuits (ASICs) are custom designed integrated circuits for a providing high performance, area, and power ratings for specific applications. The design process is sophisticated and the design cost of ASIC design is quite high compared to other IC technologies thus making it suitable for large-volume or high performance applications. The design process involves rigorous verification and validation at various stages and thus the domain provides numerous applications for formal verification methods. The scope of the conference is targeted to these areas, while particularly focusing on:

- Very Large Scale Integration Design
- Clock Multipliers
- Thermal Management in Multi-core Systems
- Automatic Software Testing
- Validation of Digital Circuits
- Timing Analysis of digital circuits
- Vertgen: Verilog Test-bench Generator
- Transform Methods for Analyzing the Safety Critical Systems
- Analyzing the Security Applications
- Accurate Analysis of Power Electronics